## DTK-01C
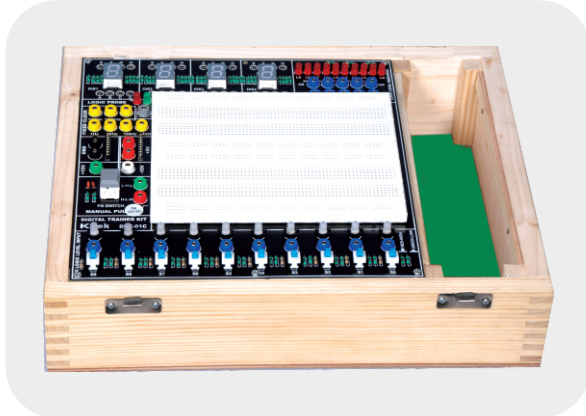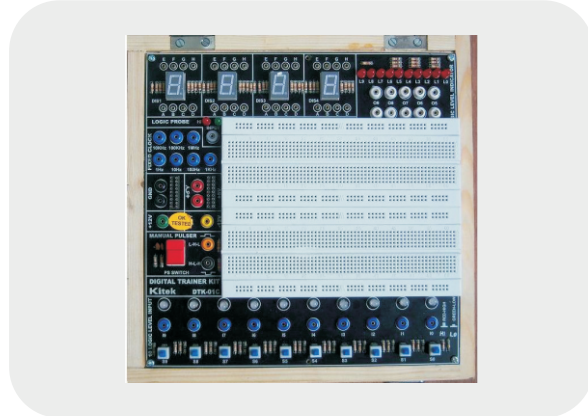## DIGITAL/ANALOG TRAINER KIT

## DTK-01D
## DIGITAL/ANALOG TRAINER KIT





### *Specifications*

- Indicators
  - 10 TTL/CMOS Logic Level Inputs with Dual Color LED indication for logic low and logic high.
  - 10 LED for output indication.
- Clock Generator
  - Fixed Clock generation of 1Hz, 10Hz, 100Hz, 1KHz, 10KHz, 100KHz & 1MHz.
- Pulse Generator
  - Logic Pulsar provides single pole double throw bounce less pulses of Low to High and High to Low transitions
- Logic Probe
  - Logic Probe facility provided with LED indicators to indicate Logic High, Low & Tri-state.
- Bread Board Area
  - Four Distribution Strip of 100 tie points each totaling 400 tie points
  - Two Terminal Strip of 630 tie points totaling 1260 tie points.
- Display
  - Four digit seven-segment LED display.
- Power Supplies
  - Fixed DC Power Supply: +5V, ±12V.
- Interconnections
  - All interconnections are made using 2mm banana Patch cords.
- All ICS are mounted on IC Sockets.
- Bare board Tested Glass Epoxy SMOBC PCB is used.
- Attractive Wooden enclosures of Light weight Australian Pine Wood.
- Set of 2mm Patch cords for interconnections
- User's Manual with sample experimental programs

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- Indicators
  - 10 TTL/CMOS Logic Level Inputs with Dual Color LED indication for logic low and logic high
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- Pulse Generator
  - Logic Pulsar provides single pole double throw bounce less pulses of Low to High and High to Low transitions
- Logic Probe
  - Logic Probe facility provided with LED indicators to indicate Logic High, Low & Tri-state
- Bread Board Area
  - Four Distribution Strip of 100 tie points each totaling 400 tie points
  - Two Terminal Strip of 630 tie points totaling 1260 tie points
- Display
  - Four digit BCD seven-segment LED display
- Power Supplies
  - Fixed DC Power Supply: +5V, ±12V.
- Interconnections
  - All interconnections are made using 2mm banana Patch cords.
- All ICS are mounted on IC Sockets.
- Bare board Tested Glass Epoxy SMOBC PCB is used.
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*Note : Specifications can be altered without notice in our constant efforts for improvement.*