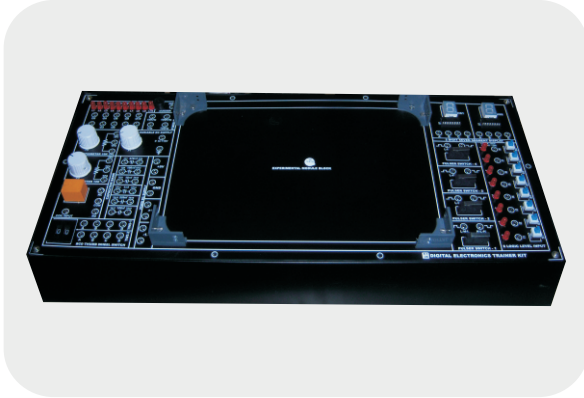


KMS-01A DIGITAL ELECTRONICS TRAINER KIT



KMS-01A is Versatile trainer which is capable of performing the role of Digital IC Trainer, Gates, Flip flop, synchronous, ADC, DAC, Decoder, Multiplexer, Eprom, RAM, FPGA and CPLD.

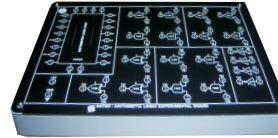
Specifications

- 8 TTL/CMOS Logic Level Inputs with indicator LED for Logic Low & Logic High.
- 10 output LED's for Logic level Indicators.
- Fixed (TTL) Clock generator of 1Hz, 100Hz, 10KHz, 100KHz.
- Four Pulser Generator with Eight debounced Output.
- Two digit BCD to 7 segment decoder.
- On-board two 10K Variable Potentiometer
- On-board +12V SPDT Relay for Control application.
- On-board two BCD Thumbwheel switch.
- On-board Buzzer for alarm application.
- On-board 1280 TIE points Bread Board.
- On-board Capacitor of 100nf, 220nf, 1uf, 10uf, 100uf, 470uf.
- Fixed DC Power Supply: +5V/1A, ±12V/250mA.
- Variable DC Power supply of 0 to +15V/250mA output.
- All Inputs/Outputs are brought on 2mm socket.
- Set of 2mm Banana Patch Cords.
- Should support DN series ready to use Electronics board.
- Bare board Tested Glass Epoxy SMOBC PCB is used.
- User's Manual.

Note : Specifications can be altered without notice in our constant efforts for improvement.

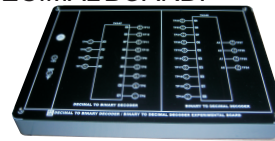
Digital experimental boards:

1. DN-01 GATES/ARITHMETIC LOGIC BOARD



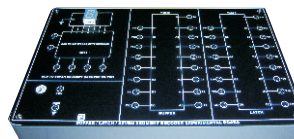
- Experiments on AND, OR, NOR, NAND gates.
- Experiments on NOT, Ex-OR, EX-NOR.
- Experiments on ALU

2. DN-02 DEICIMAL TO BINARY & BINARY TO DECIMAL BOARD.



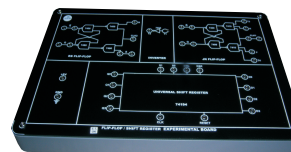
- Experiments on Binary to Decimal convertor.
- Experiments on Decimal to Binary convertor.

3. DN-03 BUFFER/LATCH/ SEVEN SEGMENT DECODER BOARD



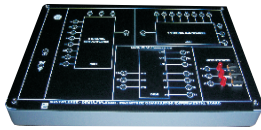
- Experiments on Buffer output.
- Experiments on Latch output.
- Experiments on Seven Segment Decoder.

4. DN-04 FLIP FLOP/ SHIFT REGISTER BOARD



- Experiments on RS, T, & D Flip Flop.
- Experiments on JK Flip Flop & Master Slave JK Flip Flop.
- Experiments on Universal Shift Register.

5. DN-05 MULTIPLEXER/DEMUX/ MAGNITUDE COMPARATOR BOARD.



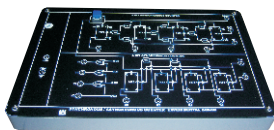
- Experiments on 8 channel Multiplexer.
- Experiments on 8 channel Demultiplexer.
- Experiments on 4 bit Magnitude Comparator.

10. EXPERIMENTAL GENERAL PURPOSE BOARD.



- 1280 tie point bread board.

6. DN-06 SYNCHRONOUS/ ASYNCHRONOUS COUNTER BOARD.



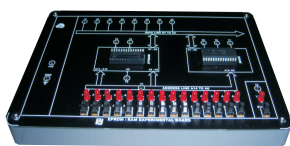
- Experiments on 4 bit Asynchronous Counter.
- Experiments on 4 bit Synchronous Counter.

7. DN-06 8 BIT ADC & DAC BOARD.



- Experiments on 8 bit Analog to Digital Converter.
- Experiments on 8 bit Digital to Analog Converter.

8. DN-08 EPROM/RAM EXPERIMENTAL BOARD



- Experiments on Program & Write operation of 64k Eprom.
- Experiments on Read & Write operation of 32K RAM.

9. DN-09 CPLD XC9572 BOARD.

- Experiments on Full Adder, Adder/ Subtractor.
- Experiments on Seven Segment Decoder.
- Experiments on Binary to Grey Conversion.
- Experiments on Comparator, Decoder, Demultiplexer.
- Experiments on Encoder, Multiplexer, Parity Generator.
- Experiments on T, D JK Flip Flop

Note : Specifications can be altered without notice in our constant efforts for improvement.